

Model Name: GA-H61M-S2P

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*1 SLOT
16	IT8892E
17	PCI SLOT1&2
18	ITE 8728 LPC IO
19	COM,KB_USB,USB_ESATA,-PROCHOT
20	HWM,FAN CTRL,OV,
21	DUAL BIOS
22	FP,FUSB,SPK,SATALED
23	Realtek ALC887-VD2
24	REAR AUDIO JACK
25	Artheros AR8151
26	DISCRETE POWER
27	ATX

SHEET

TITLE

28	RT8120_CPU_VTT
29	VCORE ISL95836_1
30	VCORE ISL95836_2
31	VCORE ISL95836_3
32	LPT

Gigabyte Technology

Cover Sheet

Size	Document Number	Rev
Custom	GA-H61M-S2P	2.01
Date:	Tuesday, December 06, 2011	Sheet 1 of 32

Revision 2.01

## Component value change history

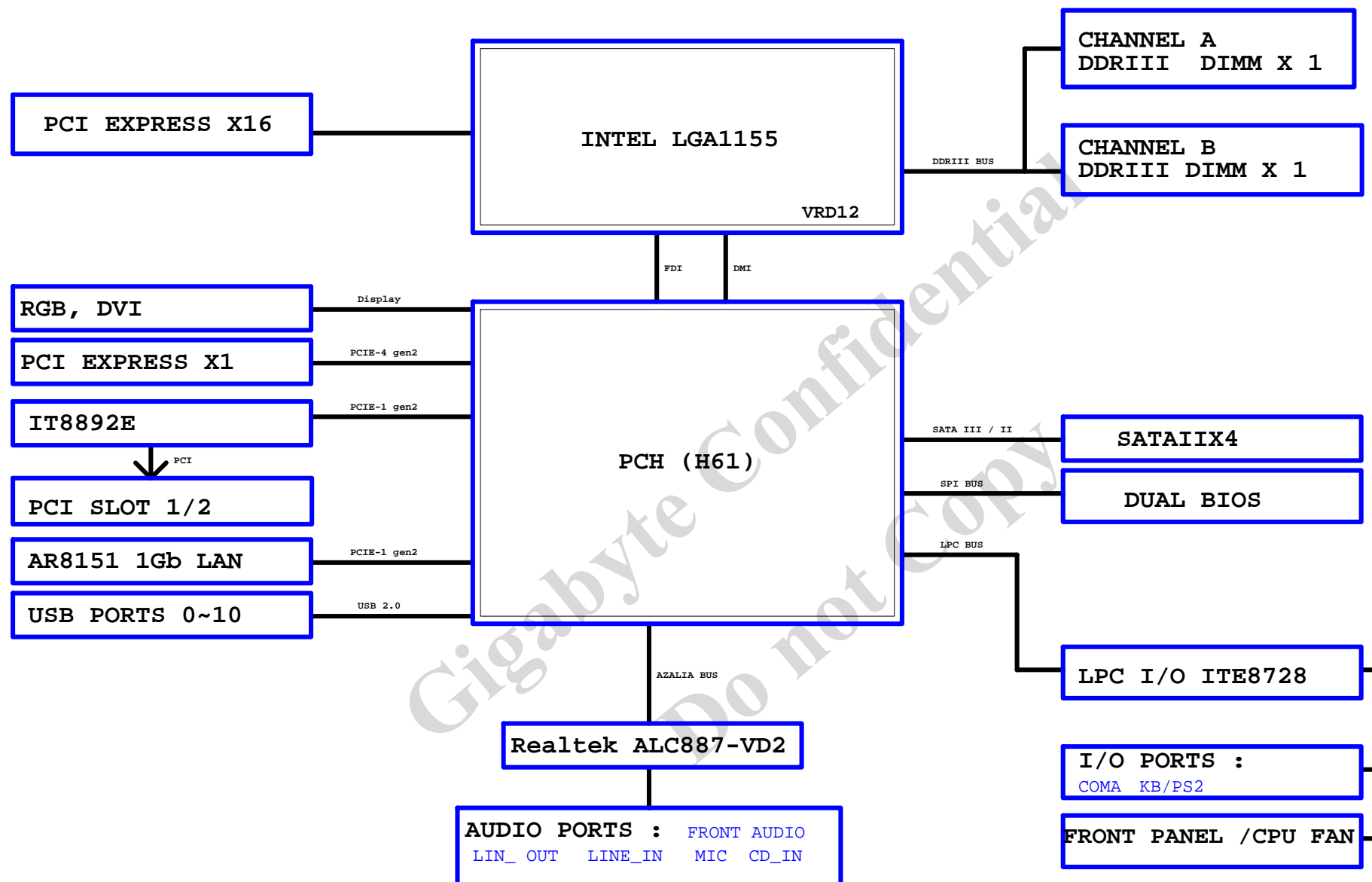
2011/11/29

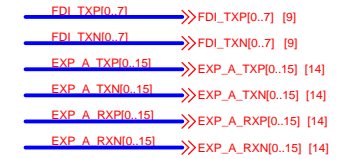
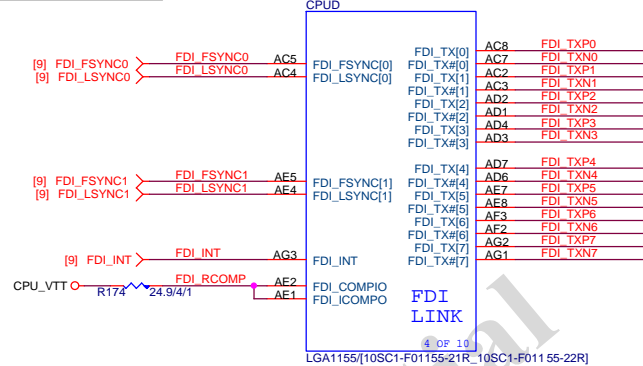
[illegible]

## Circuit or PCB layout change

[illegible]

## BLOCK DIAGRAM



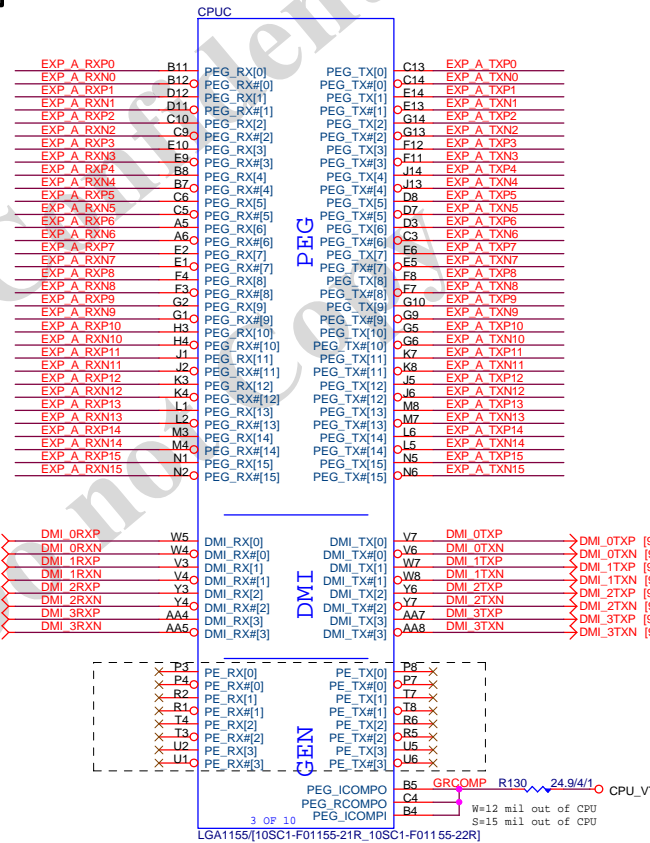


CFG	H	L	NOTE
0	RSVD	RSVD	RSVD
1	RSVD	RSVD	RSVD
2	<b>Normal</b>	<b>Reverse</b>	LANE REVERSAL[0], x16
3	RSVD	RSVD	RSVD
4	RSVD	RSVD	RSVD
7	RSVD	RSVD	RSVD
8	RSVD	RSVD	RSVD
9	RSVD	RSVD	RSVD
10	RSVD	RSVD	RSVD
11	RSVD	RSVD	RSVD
12	RSVD	RSVD	RSVD
13	RSVD	RSVD	RSVD
14	RSVD	RSVD	RSVD
15	RSVD	RSVD	RSVD
16	RSVD	RSVD	RSVD
17	RSVD	RSVD	RSVD

CFG6	CFG5	PCIE CONFIG
1	1	1x16 , Default
1	0	2X8
0	1	RSVD
0	0	X8,X4,X4

CFG 0-17 all internal PULL-UP

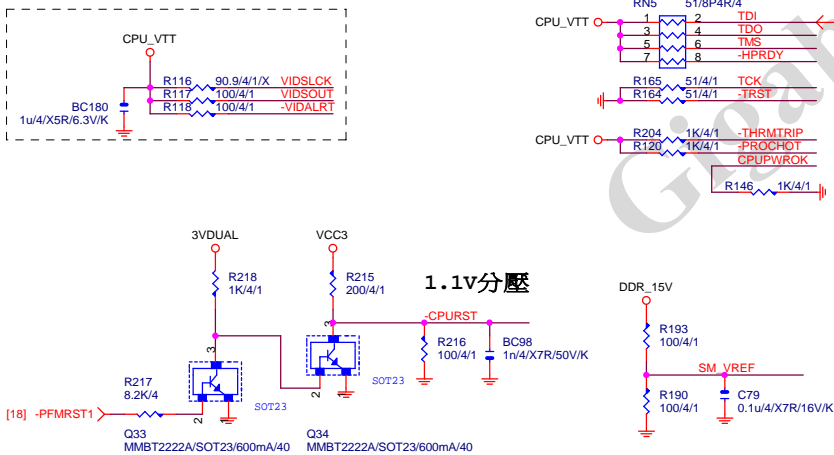
## CPU C



STITCHING CAP.

N/A

### Stitching caps for PCIE,DMI,FDI bus



## Gigabyte Technology

Title			
CPU LGA1155-A			
Size	Document Number	GA-H61M-S2P	Rev
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## CPUA

MAAA0	AV27	SA_MA[0]	SA_DQS[0]	AK3	DQSA0
MAAA1	AY24	SA_MA[1]	SA_DQS[0]	AK2	-DQSA0
MAAA2	AW24	SA_MA[2]			
MAAA3	AW23	SA_MA[3]			
MAAA4	AV23	SA_MA[3]	SA_DQ[0]	AJ3	MDA0
MAAA5	AT24	SA_MA[4]	SA_DQ[1]	AJ4	MDA1
MAAA6	AT23	SA_MA[5]	SA_DQ[2]	AL3	MDA2
MAAA7	AV22	SA_MA[6]	SA_DQ[3]	AL4	MDA3
MAAA8	AU22	SA_MA[7]	SA_DQ[4]	AJ2	MDA4
MAAA9	AT22	SA_MA[8]	SA_DQ[5]	AJ1	MDA5
MAAA10	AV28	SA_MA[10]	SA_DQ[6]	AL2	MDA6
MAAA11	AU21	SA_MA[11]	SA_DQ[7]	AL1	MDA7
MAAA12	AT21	SA_MA[12]			
MAAA13	AW32	SA_MA[13]	SA_DQS[1]	AP3	DQSA1
MAAA14	AU20	SA_MA[14]	SA_DQS[1]	AP2	-DQSA1
MAAA15	AT20	SA_MA[15]			
[7] -SWEA	AW29	SA_WE#	SA_DQ[8]	AN1	MDA8
[7] -SCASA	AV30	SA_CAS#	SA_DQ[9]	AN4	MDA9
[7] -SRASA	AU28	SA_RAS#	SA_DQ[10]	AN3	MDA10
			SA_DQ[11]	AR4	MDA12
[7] SBAA0	AY29	SA_BS[0]	SA_DQ[12]	AN2	MDA11
[7] SBAA1	AW28	SA_BS[1]	SA_DQ[13]	AN3	MDA13
[7] SBAA2	AV20	SA_BS[2]	SA_DQ[14]	AR2	MDA14
			SA_DQ[15]	AR1	MDA15
[7] -CSA0	AY29	SA_CS#0]			
[7] -CSA1	AV32	SA_CS#1]	SA_DQS[2]	AW4	DQSA2
	AW30	SA_CS#2]	SA_DQS[2]	AW4	-DQSA2
	AW33	SA_CS#3]			
[7] CKEA0	AY19	SA_CKE[0]	SA_DQ[16]	AV2	MDA16
[7] CKEA1	AT19	SA_CKE[1]	SA_DQ[17]	AW3	MDA17
	AU18	SA_CKE[2]	SA_DQ[18]	AW5	MDA18
	AV18	SA_CKE[3]	SA_DQ[19]	AW5	MDA19
			SA_DQ[20]	AU2	MDA20
	AV31	SA_ODT[0]	SA_DQ[21]	AL3	MDA21
	AW32	SA_ODT[1]	SA_DQ[22]	AY5	MDA22
	AU30	SA_ODT[2]	SA_DQ[23]	AY5	MDA23
	AW33	SA_ODT[3]			
[7] DCLKA0	AY25	SA_CK[0]	SA_DQS[3]	AV8	DQSA3
[7] -DCLKA0	AW25	SA_CK#0]	SA_DQS[3]	AW8	-DQSA3
[7] DCLKA1	AU24	SA_CK[1]			
[7] -DCLKA1	AW27	SA_CK#1]	SA_DQ[24]	AY7	MDA24
	AY27	SA_CK[2]	SA_DQ[25]	AU7	MDA25
	AW26	SA_CK[3]	SA_DQ[26]	AV9	MDA26
	AW26	SA_CK#2]	SA_DQ[27]	AU9	MDA27
	AW26	SA_CK[3]	SA_DQ[28]	AV7	MDA28
	AW26	SA_CK#3]	SA_DQ[29]	AW9	MDA29
			SA_DQ[30]	AW9	MDA30
			SA_DQ[31]	AY9	MDA31
			SA_DQS[4]	AV37	DQSA4
			SA_DQS[4]	AV36	-DQSA4
			SA_DQ[32]	AU35	MDA32
			SA_DQ[33]	AW37	MDA33
			SA_DQ[34]	AU39	MDA34
			SA_DQ[35]	AU36	MDA35
			SA_DQ[36]	AW35	MDA36
			SA_DQ[37]	AY36	MDA37
			SA_DQ[38]	AU38	MDA38
			SA_DQ[39]	AU37	MDA39
			SA_DQS[5]	AP38	DQSA5
			SA_DQS[5]	AP39	-DQSA5
			SA_DQ[40]	AR40	MDA40
			SA_DQ[41]	AR37	MDA41
			SA_DQ[42]	AN38	MDA42
			SA_DQ[43]	AN37	MDA43
			SA_DQ[44]	AR39	MDA44
			SA_DQ[45]	AR38	MDA45
			SA_DQ[46]	AN39	MDA46
			SA_DQ[47]	AN40	MDA47
			SA_DQS[6]	AK38	DQSA6
			SA_DQS[6]	AK39	-DQSA6
			SA_DQ[48]	AL40	MDA48
			SA_DQ[49]	AL37	MDA49
			SA_DQ[50]	AJ38	MDA50
			SA_DQ[51]	AJ37	MDA51
			SA_DQ[52]	AL38	MDA52
			SA_DQ[53]	AJ39	MDA53
			SA_DQ[54]	AJ39	MDA54
			SA_DQ[55]	AJ40	MDA55
			SA_DQS[7]	AF38	DQSA7
			SA_DQS[7]	AF39	-DQSA7
			SA_DQ[56]	AG40	MDA56
			SA_DQ[57]	AG37	MDA57
			SA_DQ[58]	AE38	MDA58
			SA_DQ[59]	AE37	MDA59
			SA_DQ[60]	AG39	MDA60
			SA_DQ[61]	AG38	MDA61
			SA_DQ[62]	AE39	MDA62
			SA_DQ[63]	AE40	MDA63

DDR\_0

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LGA1155[10SC1-F01155-21R\_10SC1-F01155-22R]

## CPUB

MAAB0	AK24	SB_MA[0]	SB_DQS[0]	AH7	DQSB0
MAAB1	AM20	SB_MA[1]	SB_DQS[0]	AH6	-DQSB0
MAAB2	AM19	SB_MA[2]			
MAAB3	AK18	SB_MA[3]			
MAAB4	AP19	SB_MA[4]	SB_DQ[0]	AG7	MDB0
MAAB5	AP18	SB_MA[5]	SB_DQ[1]	AG8	MDB1
MAAB6	AM18	SB_MA[6]	SB_DQ[2]	AJ9	MDB2
MAAB7	AM18	SB_MA[7]	SB_DQ[3]	AJ8	MDB3
MAAB8	AN15	SB_MA[8]	SB_DQ[4]	AG5	MDB4
MAAB9	AY17	SB_MA[9]	SB_DQ[5]	AG6	MDB5
MAAB10	AN23	SB_MA[10]	SB_DQ[6]	AJ6	MDB6
MAAB11	AU17	SB_MA[11]	SB_DQ[7]	AJ7	MDB7
MAAB12	AT18	SB_MA[12]			
MAAB13	AR26	SB_MA[13]	SB_DQS[1]	AM8	DQSB1
MAAB14	AY16	SB_MA[14]	SB_DQS[1]	AL8	-DQSB1
MAAB15	AV16	SB_MA[15]			
[8] -SWEB	AR25	SB_WE#	SB_DQ[8]	AL7	MDB8
[8] -SCASB	AK25	SB_CAS#	SB_DQ[9]	AM7	MDB9
[8] -SRASB	AP24	SB_RAS#	SB_DQ[10]	AM10	MDB10
			SB_DQ[11]	AL10	MDB11
			SB_DQ[12]	AL9	MDB12
[8] SBAB0	AP23	SB_BS[0]	SB_DQ[13]	AM6	MDB13
[8] SBAB1	AM24	SB_BS[1]	SB_DQ[14]	AM9	MDB14
[8] SBAB2	AW17	SB_BS[2]	SB_DQ[15]	AM9	MDB15
[8] -CSB0	AN25	SB_CS#0]			
[8] -CSB1	AN26	SB_CS#1]	SB_DQS[2]	AR8	DQSB2
	AL25	SB_CS#2]	SB_DQS[2]	AP8	-DQSB2
	AT26	SB_CS#3]			
[8] CKEB0	AU16	SB_CKE[0]	SB_DQ[16]	AP7	MDB16
[8] CKEB1	AY15	SB_CKE[1]	SB_DQ[17]	AR7	MDB17
	AW15	SB_CKE[2]	SB_DQ[18]	AP10	MDB18
	AY15	SB_CKE[3]	SB_DQ[19]	AP10	MDB19
			SB_DQ[20]	AP6	MDB20
	AL26	SB_ODT[0]	SB_DQ[21]	AR6	MDB21
	AP26	SB_ODT[1]	SB_DQ[22]	AP9	MDB22
	AM26	SB_ODT[2]	SB_DQ[23]	AR9	MDB23
	AK26	SB_ODT[3]			
			SB_DQS[3]	AN13	DQSB3
			SB_DQS[3]	AN12	-DQSB3
[8] DCLKB0	AL21	SB_CK[0]	SB_DQ[24]	AM12	MDB24
[8] -DCLKB0	AL22	SB_CK#0]	SB_DQ[25]	AM13	MDB25
[8] DCLKB1	AL20	SB_CK[1]	SB_DQ[26]	AR13	MDB26
[8] -DCLKB1	AK20	SB_CK#1]	SB_DQ[27]	AP13	MDB27
	AL23	SB_CK[2]	SB_DQ[28]	AL12	MDB28
	AM22	SB_CK#2]	SB_DQ[29]	AL12	MDB29
	AK22	SB_CK[3]	SB_DQ[30]	AR12	MDB30
	AN21	SB_CK#3]	SB_DQ[31]	AP12	MDB31
			SB_DQS[4]	AN29	DQSB4
			SB_DQS[4]	AN28	-DQSB4
			SB_DQ[32]	AR26	MDB32
			SB_DQ[33]	AR29	MDB33
			SB_DQ[34]	AL28	MDB34
			SB_DQ[35]	AL29	MDB35
			SB_DQ[36]	AP28	MDB36
			SB_DQ[37]	AP28	MDB37
			SB_DQ[38]	AM28	MDB38
			SB_DQ[39]	AM29	MDB39
			SB_DQS[5]	AP33	DQSB5
			SB_DQS[5]	AR33	-DQSB5
			SB_DQ[40]	AP32	MDB40
			SB_DQ[41]	AP31	MDB41
			SB_DQ[42]	AP35	MDB42
			SB_DQ[43]	AP34	MDB43
			SB_DQ[44]	AR32	MDB44
			SB_DQ[45]	AR31	MDB45
			SB_DQ[46]	AR35	MDB46
			SB_DQ[47]	AR34	MDB47
			SB_DQS[6]	AL33	DQSB6
			SB_DQS[6]	AM33	-DQSB6
			SB_DQ[48]	AM32	MDB48
			SB_DQ[49]	AM31	MDB49
			SB_DQ[50]	AL35	MDB50
			SB_DQ[51]	AL32	MDB51
			SB_DQ[52]	AM34	MDB52
			SB_DQ[53]	AL31	MDB53
			SB_DQ[54]	AM35	MDB54
			SB_DQ[55]	AL34	MDB55
			SB_DQS[7]	AG35	DQSB7
			SB_DQS[7]	AG34	-DQSB7
			SB_DQ[56]	AH35	MDB56
			SB_DQ[57]	AH34	MDB57
			SB_DQ[58]	AE34	MDB58
			SB_DQ[59]	AE35	MDB59
			SB_DQ[60]	AJ35	MDB60
			SB_DQ[61]	AJ34	MDB61
			SB_DQ[62]	AF33	MDB62
			SB_DQ[63]	AF35	MDB63

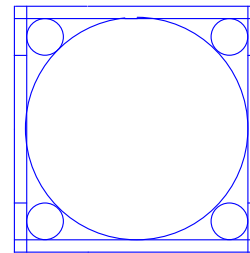
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LGA1155[10SC1-F01155-21R\_10SC1-F01155-22R]

CR

CPU RETAINION/X



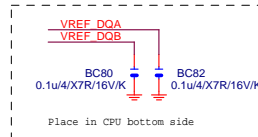
Need check the new CPU ME

CPU\_P



ILM\_BP/1156/CSP/ILM\_BP/1156/CSP/[12KRC-0F0001-05R\_12KRC-0F0001-31R]

DDR SIGNAL



[7] MODT\_A[0..1] &lt;-- MODT\_A[0..1]

[8] MODT\_B[0..1] &lt;-- MODT\_B[0..1]

[7] MDA[0..63] &lt;-- MDA[0..63]

[8] MDB[0..63] &lt;-- MDB[0..63]

[7] DQSA[0..7] &lt;-- DQSA[0..7]

[7] -DQSA[0..7] &lt;-- -DQSA[0..7]

[7] MAA[0..15] &lt;-- MAA[0..15]

[8] MAB[0..15] &lt;-- MAB[0..15]

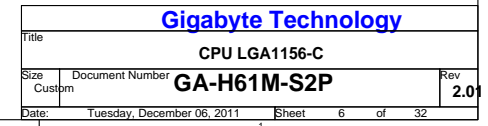
[8] DQSB[0..7] &lt;-- DQSB[0..7]

[8] -DQSB[0..7] &lt;-- -DQSB[0..7]

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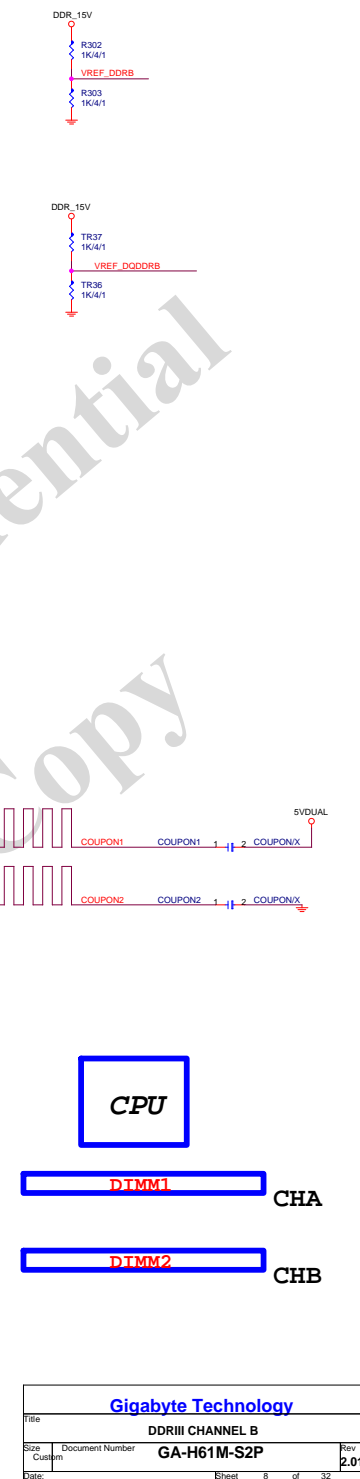
CPU LGA1156-B

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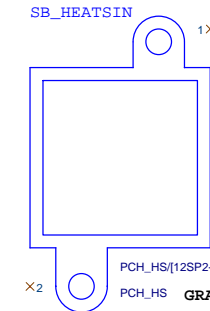
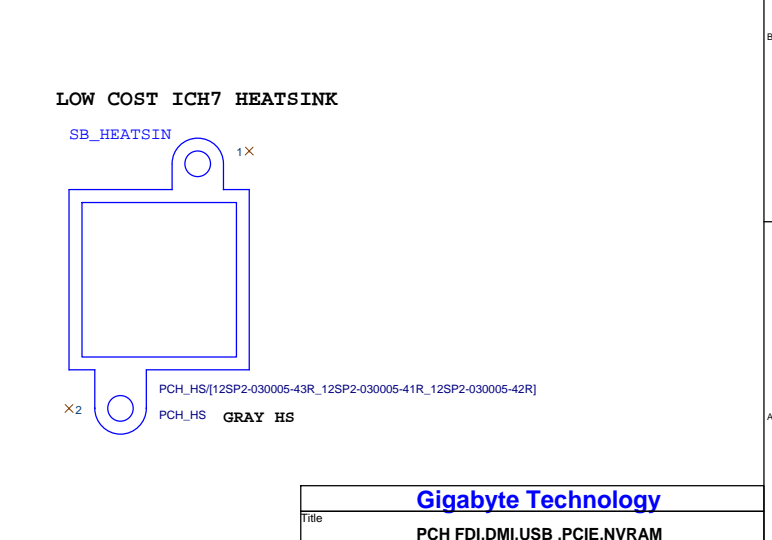
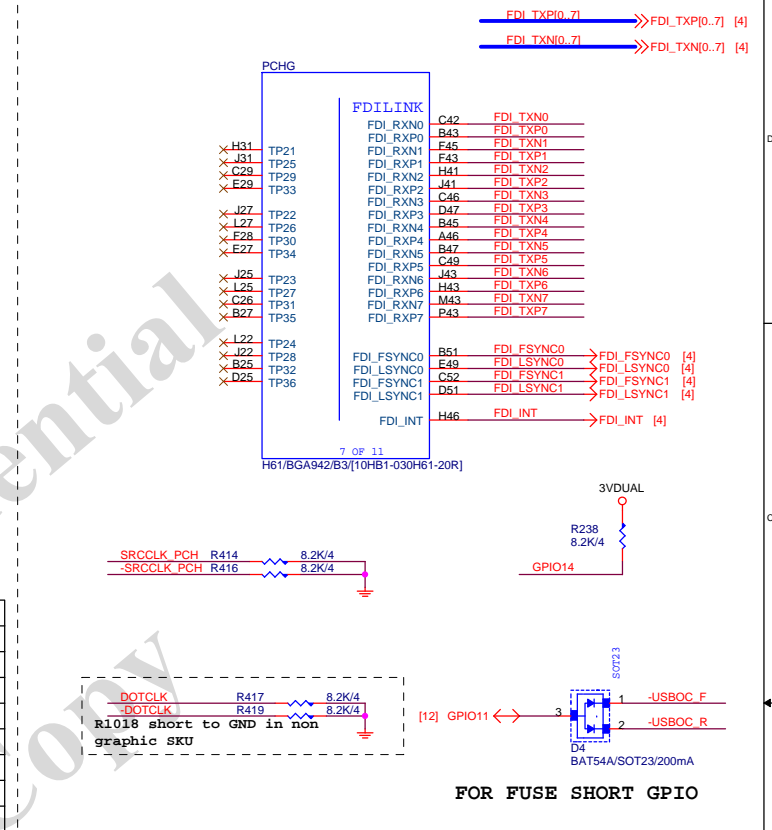




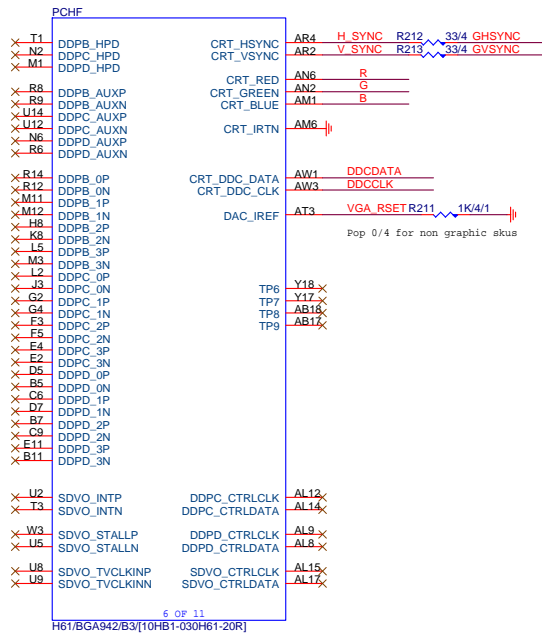






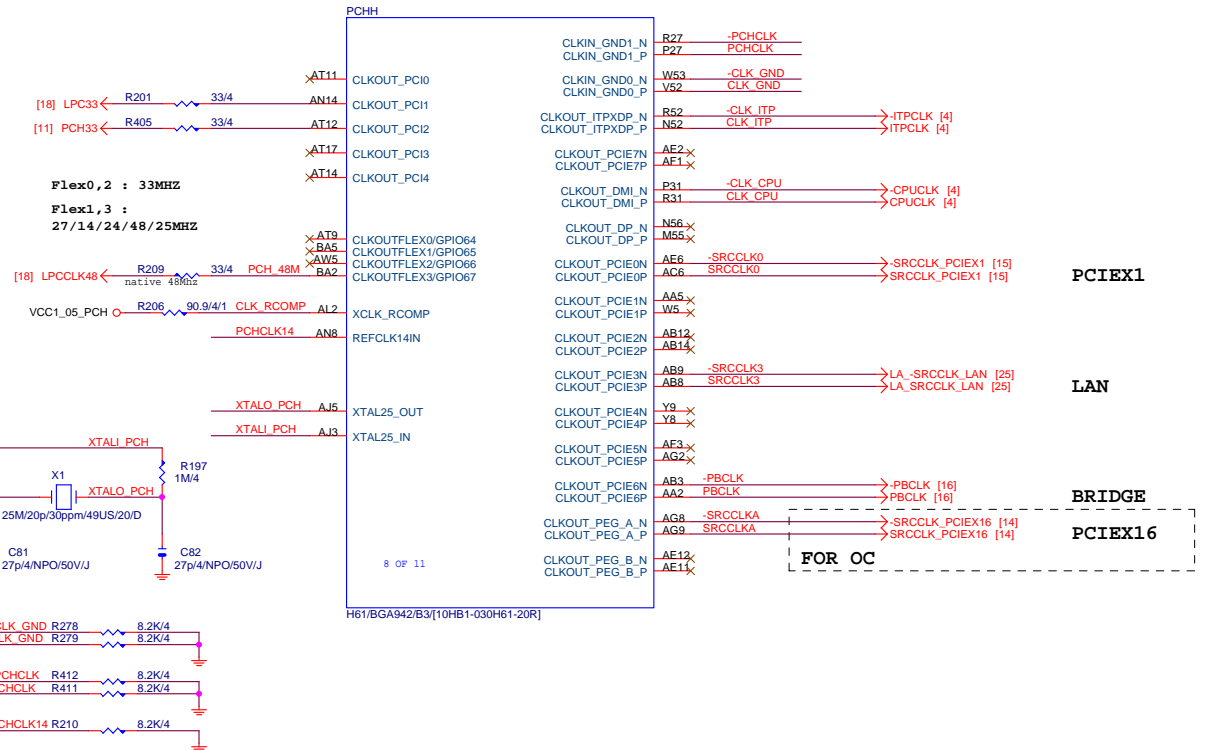


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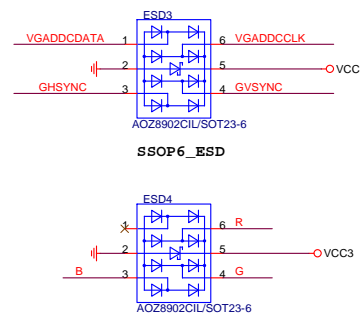


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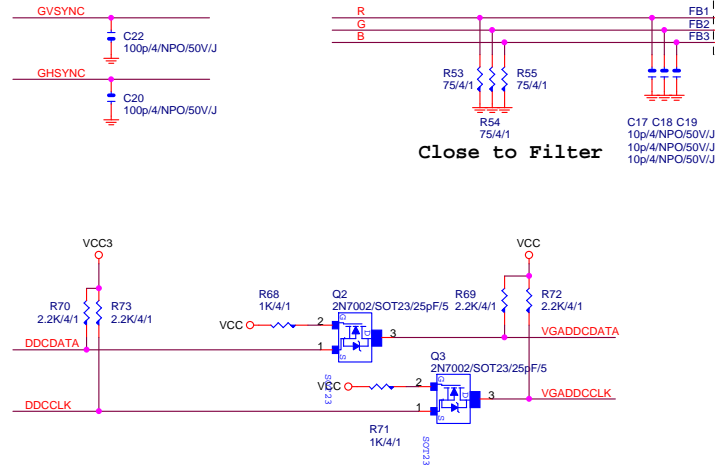
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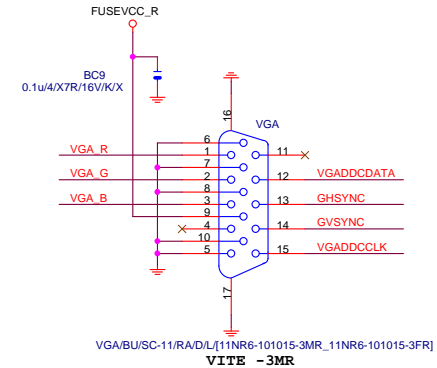
## VGA ESD



## VGA SIGNAL



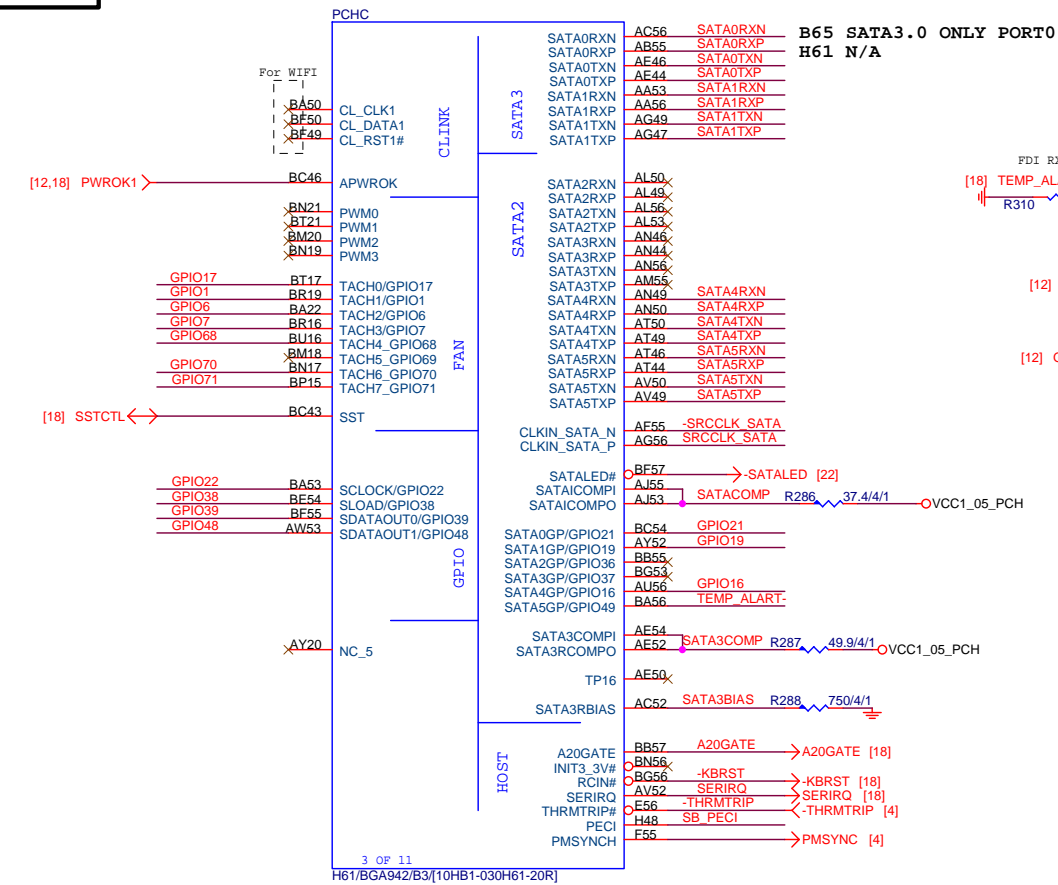
## D-SUB



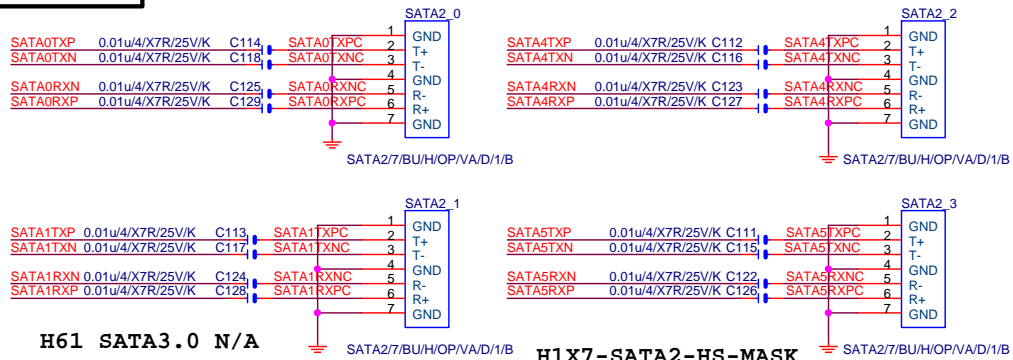
Gigabyte Technology

Title				
PCH DISPLAY ,CLK BUFFER				
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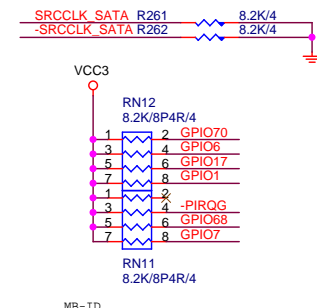
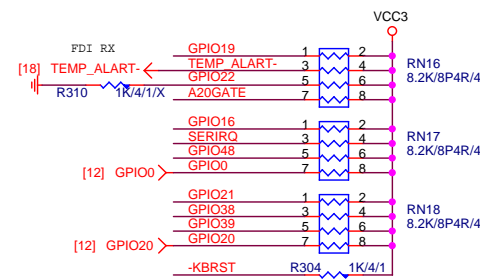
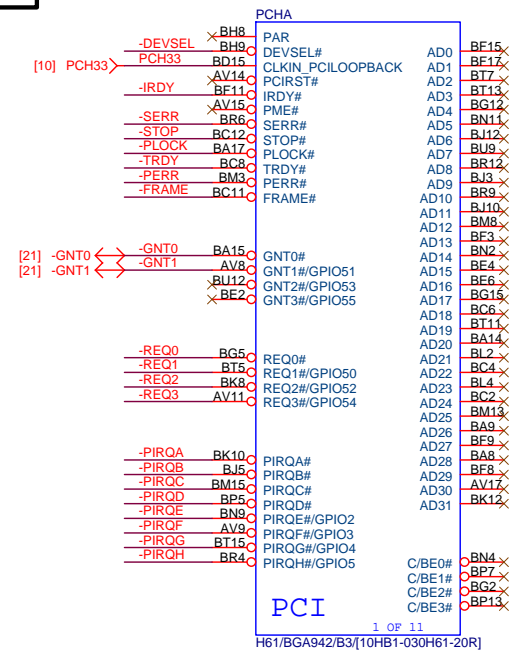
PCH C



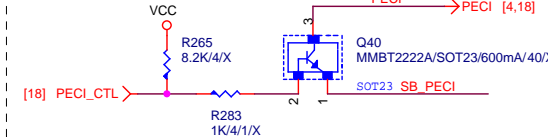
SATA CONN.



PCH A



N/A

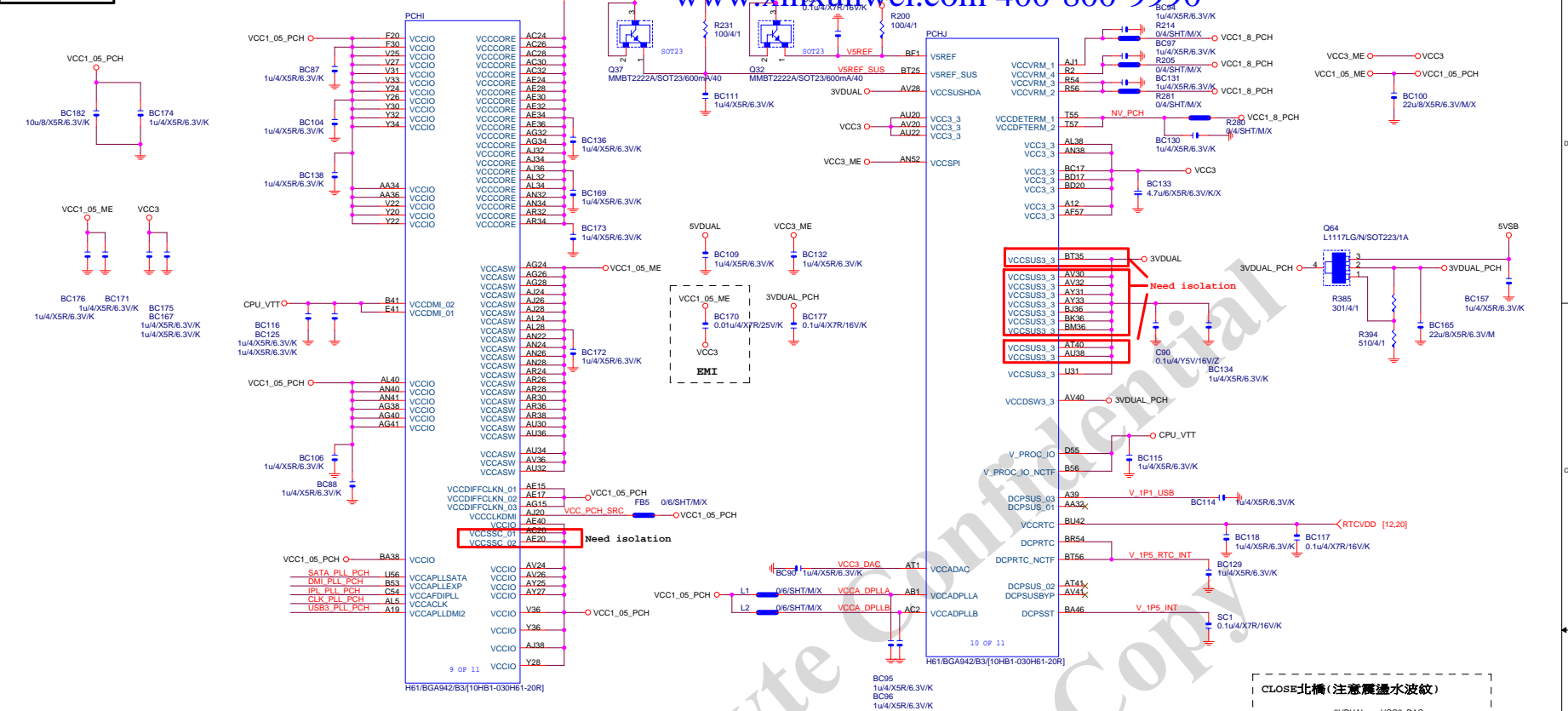


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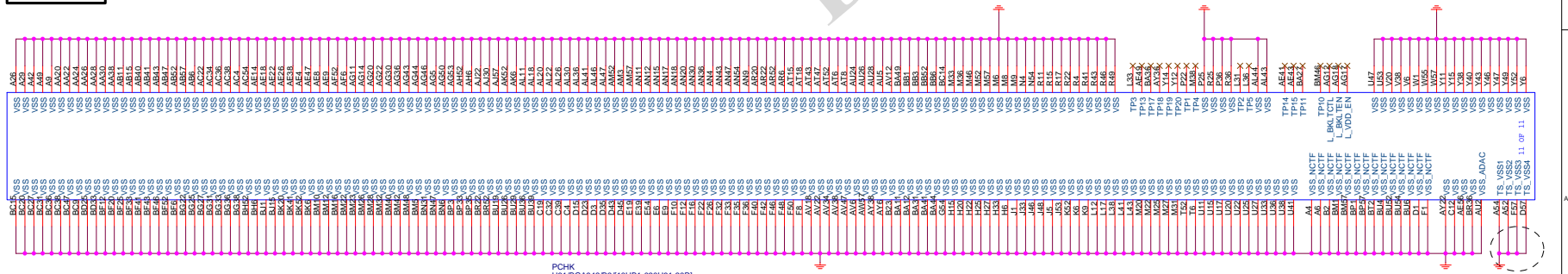
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Size B	Document Number	GA-H61M-S2P	2.01
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PCH I POWER

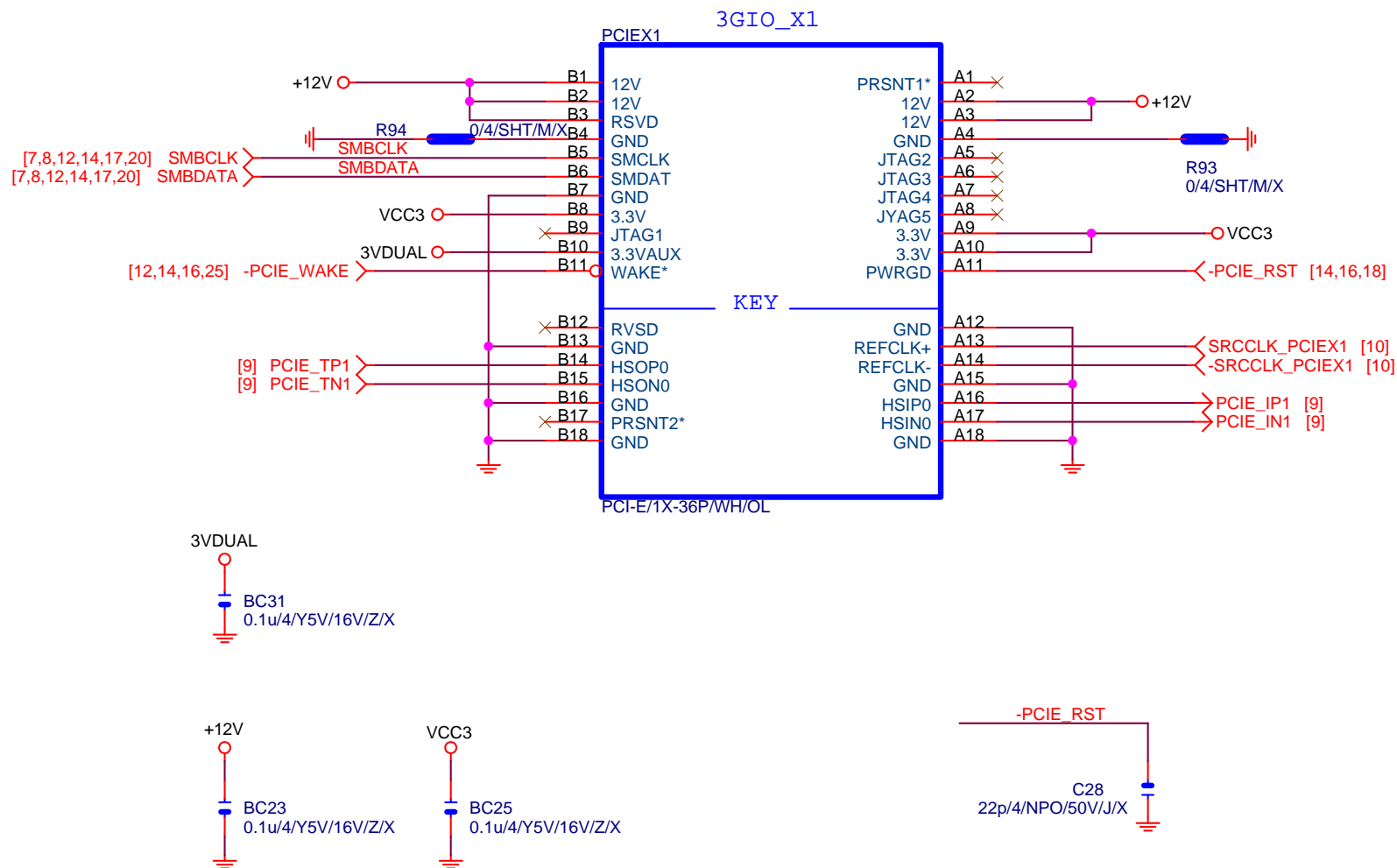


PCH K GND







**Gigabyte Technology**

Title

**PCI EXPRESS X 1 PORT**Size  
A

Document Number

**GA-H61M-S2P**Rev  
**2.01**

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PCI:5/4/5 Impedance=50 +- 15%

BA\_D0\_311 &lt;-&gt; BA\_D0\_311 [17]

-BC\_BE0 > BC\_BE0 [17]  
-BC\_BE1 > BC\_BE1 [17]  
-BC\_BE2 > BC\_BE2 [17]  
-BC\_BE3 > BC\_BE3 [17]

-BPERR > BPERR [17]  
-BSERR > BSERR [17]

-BPAR > BPAR [17]  
-BPCLOCK > BPCLOCK [17]  
-BDEVSEL > BDEVSEL [17]  
-BSTOP > BSTOP [17]  
-BTRDY > BTRDY [17]  
-BIRDY > BIRDY [17]  
-BFRAME > BFRAME [17]

-PCIE\_RST &gt; PCIE\_RST [14,15,18]

-BPCIRST &gt; BPCIRST [17]

-BREQ0 > BREQ0 [17]  
-BREQ1 > BREQ1 [17]  
-BGNT0 > BGNT0 [17]  
-BGNT1 > BGNT1 [17]

-BPCPME1 &gt; BPCPME1 [17]

Co-Lay IT8893 (IT8893 CLKOUT1 N/A)

IT8892: PR24 -&gt; 47ohm

IT8893: PR24 -&gt; 22ohm

[17] BPCLK0 &lt;-&gt; PR24 47/4/1 CLKOUT0

PR46 22/4/X IT8892: PR46 -&gt; X

IT8893: PR46 -&gt; O

[17] BPCLK1 &lt;-&gt; PR19 47/4/1 CLKOUT1

IT8892: PR19 -&gt; O

IT8893: PR19 -&gt; X

RREF PR13 12K/4/1

TEST\_EN PR21 10K/4/1

EXT\_ARB PR22 10K/4/1

RST\_SEL PR7 10K/4/1

High: Enable PCI CLK 66MHz  
Low: Disable PCI CLK 66MHz

High: PCICLK INPUT form CLK Gen  
Low: PCICLK OUTPUT form IT8893 chip

IT8892

PCI slot

chipset side

PCI slot

chipset side

chipset side

chipset side

chipset side

chipset side

chipset side

chipset side

chipset side

chipset side

chipset side

chipset side

chipset side

chipset side

chipset side

chipset side

chipset side

PRN1 2.2K/8P4R/4

PRN2 2.2K/8P4R/4

PRN3 2.2K/8P4R/4

PRN4 2.2K/8P4R/4

PRN5 2.2K/8P4R/4

PRN6 2.2K/8P4R/4

PRN7 2.2K/8P4R/4

PRN8 2.2K/8P4R/4

PRN9 2.2K/8P4R/4

PRN10 2.2K/8P4R/4

PRN11 2.2K/8P4R/4

PRN12 2.2K/8P4R/4

PRN13 2.2K/8P4R/4

PRN14 2.2K/8P4R/4

PRN15 2.2K/8P4R/4

PRN16 2.2K/8P4R/4

PRN17 2.2K/8P4R/4

PRN18 2.2K/8P4R/4

PRN19 2.2K/8P4R/4

PRN20 2.2K/8P4R/4

PRN21 2.2K/8P4R/4

PRN22 2.2K/8P4R/4

PRN23 2.2K/8P4R/4

PRN24 2.2K/8P4R/4

PRN25 2.2K/8P4R/4

PRN26 2.2K/8P4R/4

PRN27 2.2K/8P4R/4

PRN28 2.2K/8P4R/4

PRN29 2.2K/8P4R/4

PCIEWAKE

BPCIPME

VCCP

LDOAUX 18V

1.8V\_AUX

NC

CLKN

VCC18A

VCC18A

GND

RREF

1.8V\_AUX

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

WAKE#

PME#

GNDP\_AUX

VCCP\_AUX

LDOAUX 18V

VSS\_AUX

VCCP\_AUX

NC

CLKN

VCC18A

VCC18A

GND

RREF

1.8V\_AUX

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

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VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

VCCP

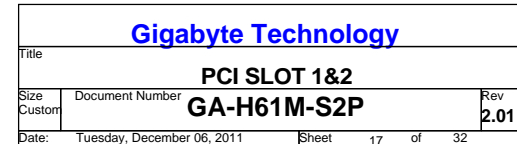
IT8892E/BX LQFP128

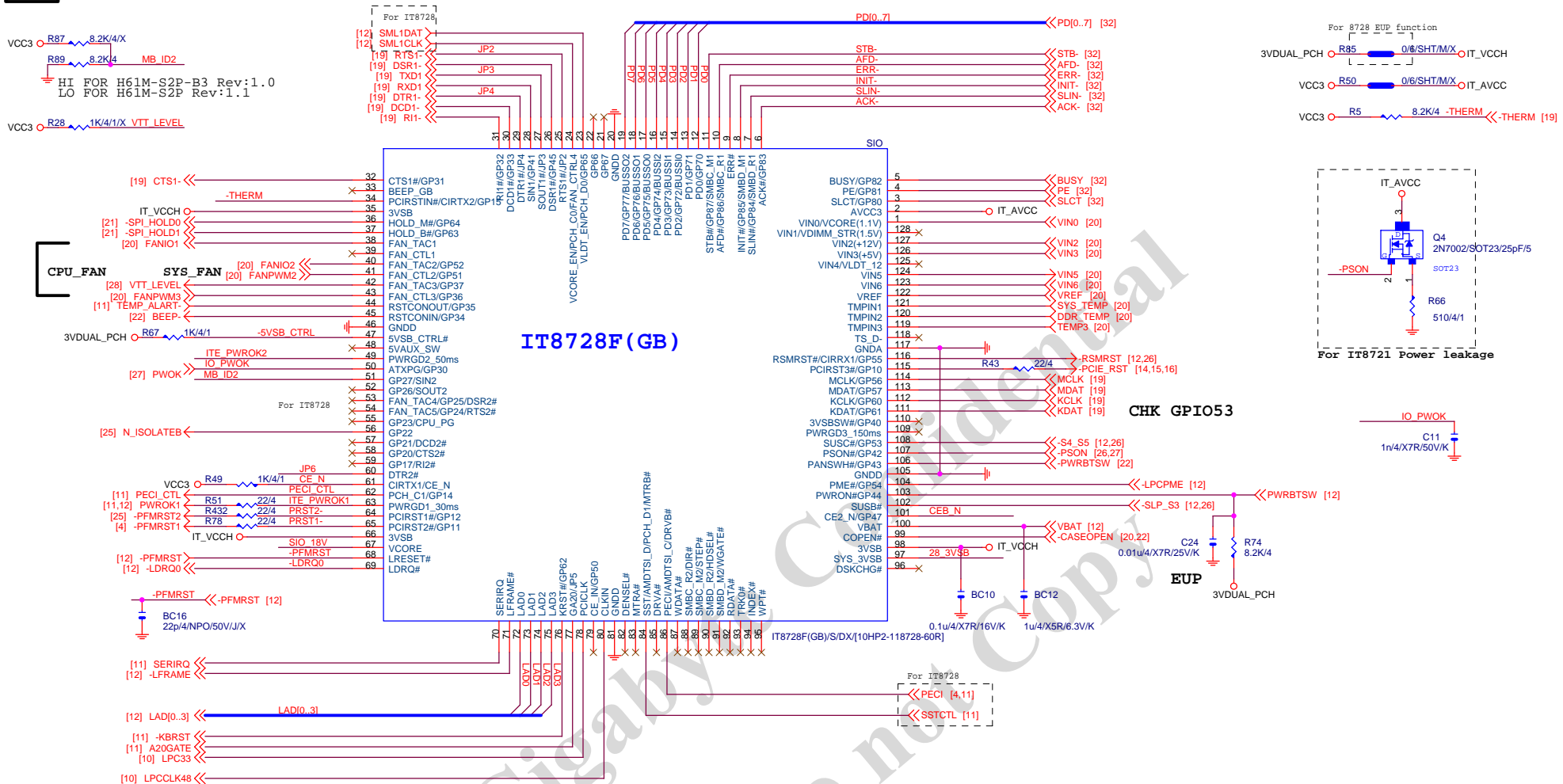
IT8892E/EX/S[10HP2-698892-30R]

PCB layout note:  
Close to chip

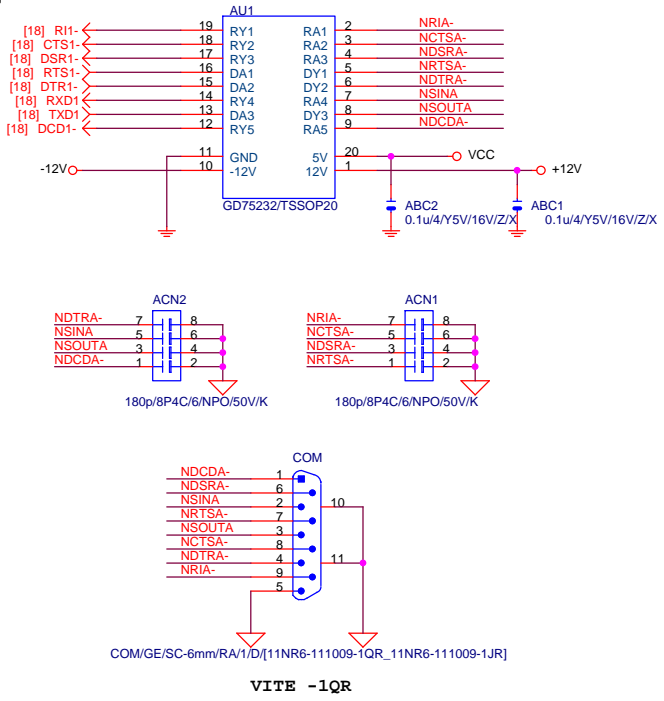
Gigabyte Technology

Title			
ITE IT8892E			
Size	Document Number		Rev
Custom	GA-H61M-S2P		2.01
Date:	Tuesday, December 06, 2011	Sheet	16 of 32

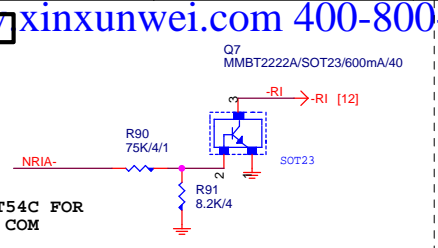




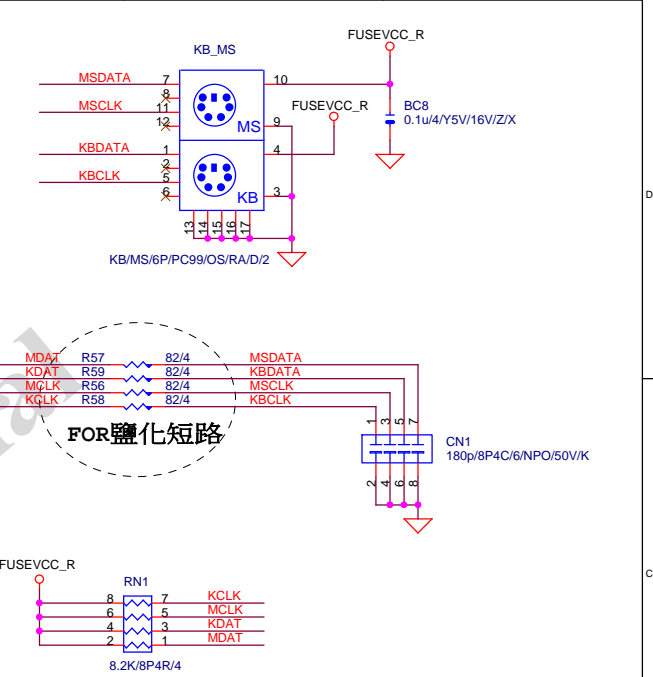
COM



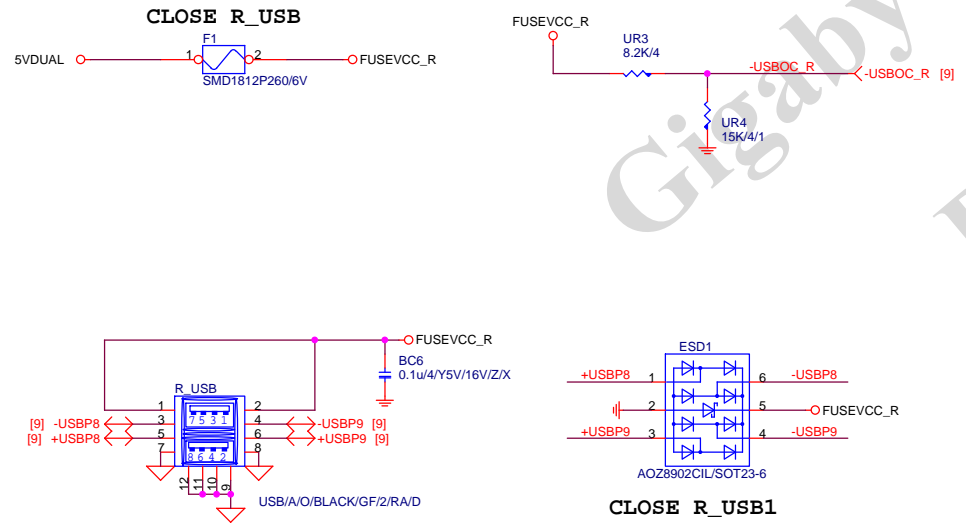
COM/RI



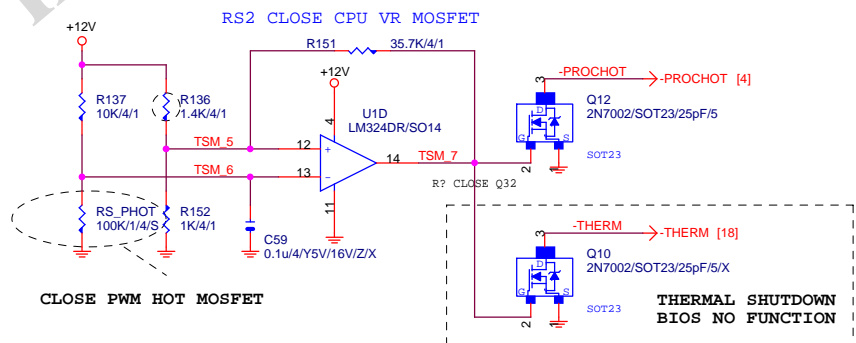
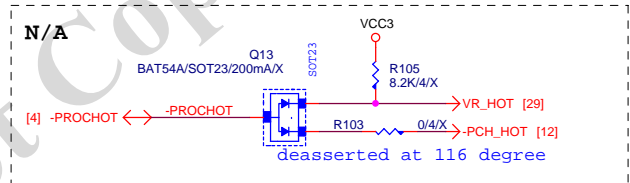
KB/MS



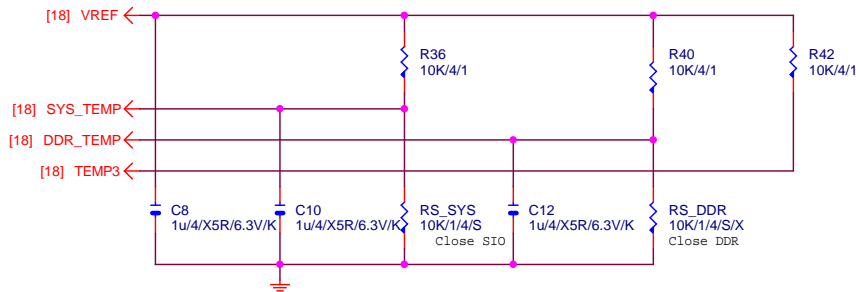
R\_USB1



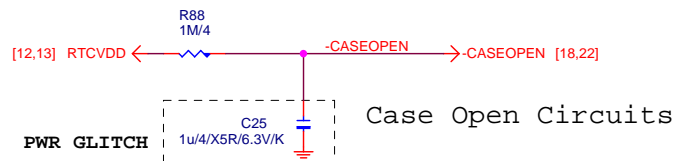
-PROHOT



# TEMP H/W MONITOR

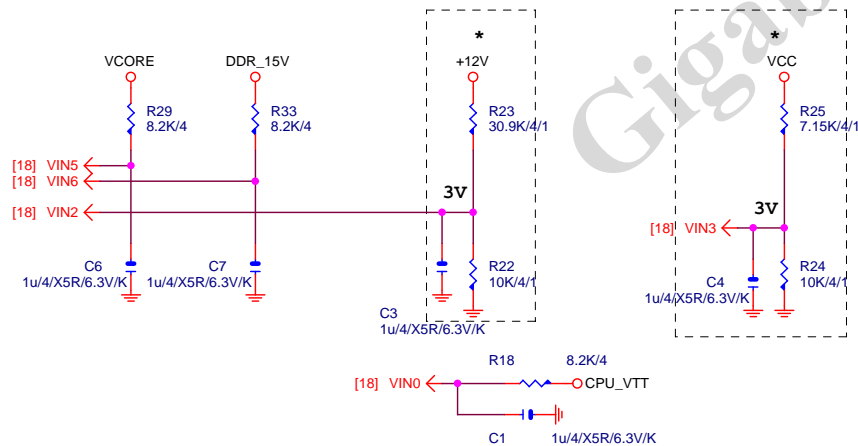


# CASE OPEN

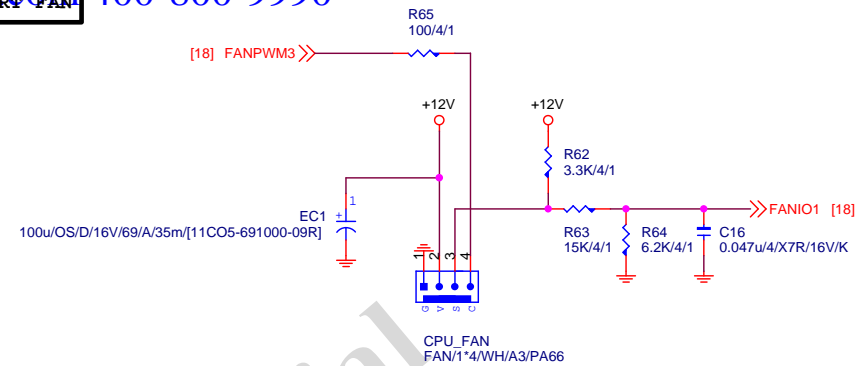


# VOLTAGE-- H/W MONITOR

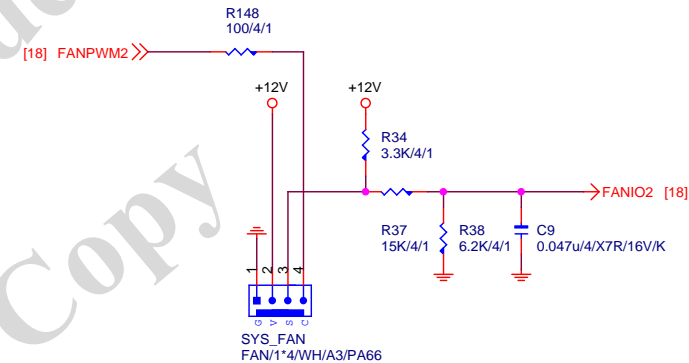
IT8728/EX VIN2/VIN3-->2V



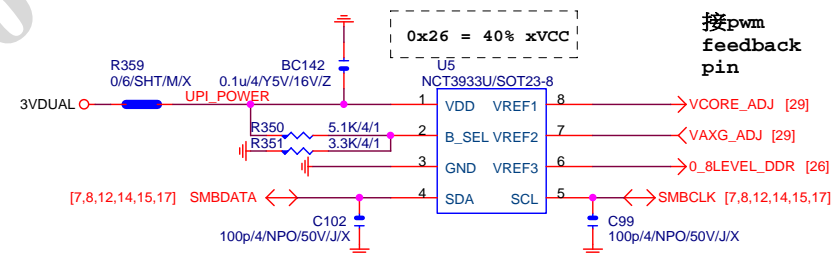
# CPU SMART FAN



# SYS SMART FAN



# O.V.

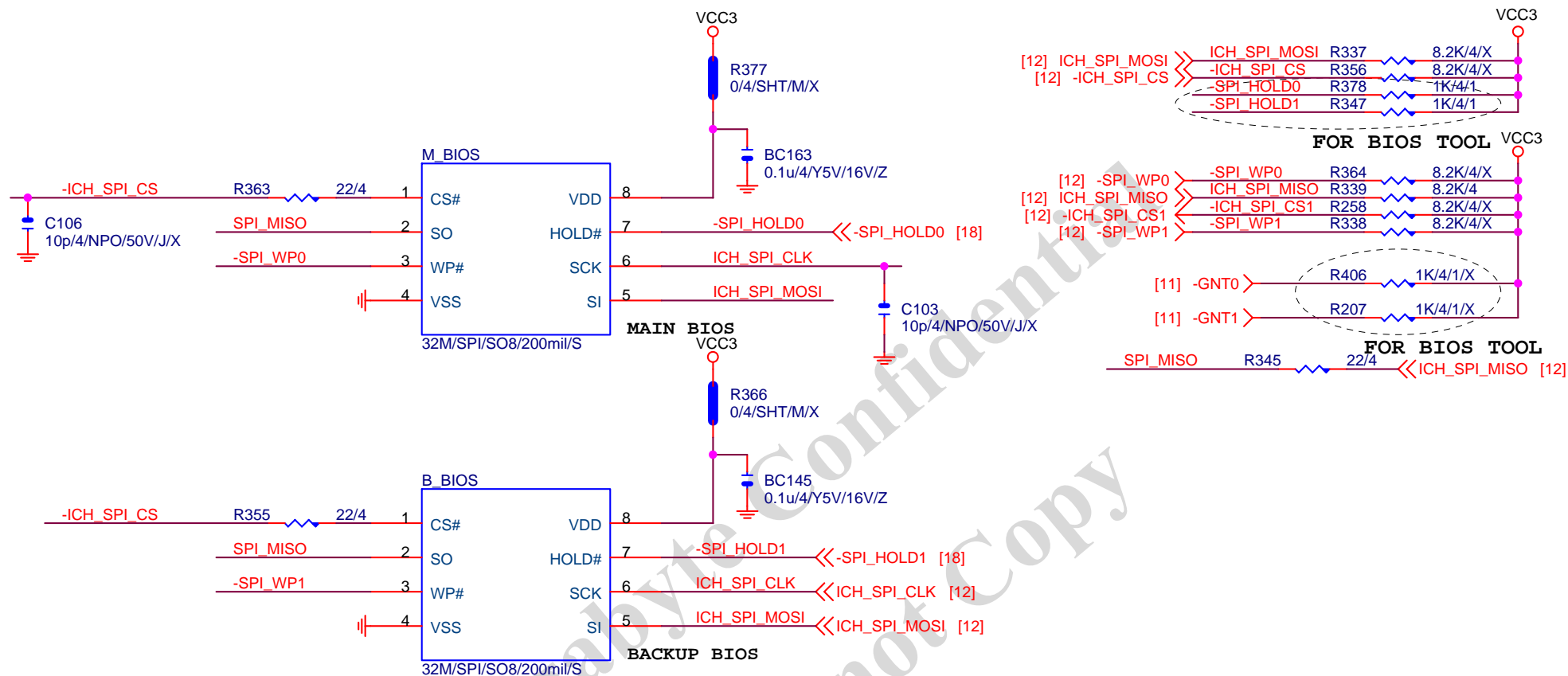


Gigabyte Technology

Title		HWM,FAN CTRL,OV	
Size	Custom	Document Number	GA-H61M-S2P
Date:	Tuesday, December 06, 2011	Sheet	20 of 32
		Rev	2.01

# DUAL BIOS

www.xinxunwei.com 400-800-9990



B65使用64M BIOS

使用H67暫用32M

H61使用32M BIOS

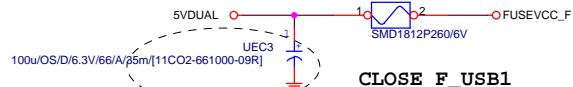
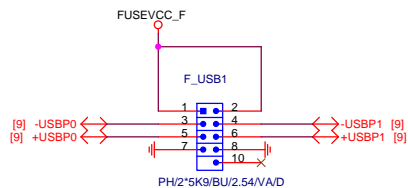
BOOT DEVICE	GNT1	GNT0
LPC	0	0
PCI	0	1
SPI	1	1

1 means floating  
0 means PD 1K

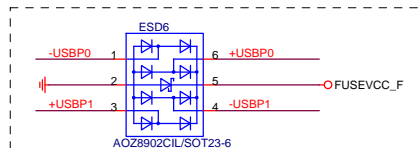
**Gigabyte Technology**

Title		
DUAL BIOS		
Size A	Document Number <b>GA-H61M-S2P</b>	Rev <b>2.01</b>
Date:	Tuesday, December 06, 2011	Sheet 21 of 32

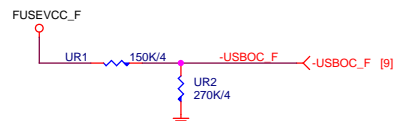
## FRONT USB1



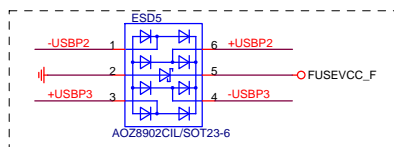
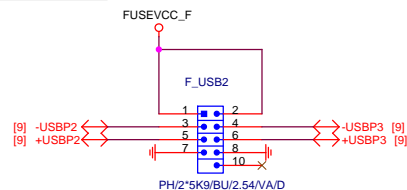
CLOSE F\_USB1



Close to connector

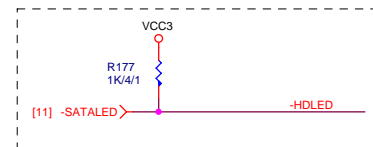


## FRONT USB2

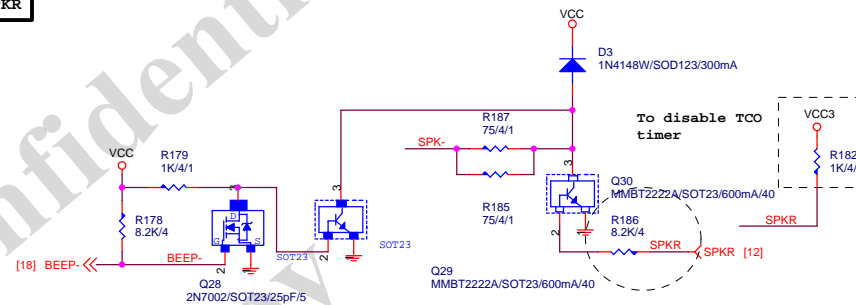


Close to connector

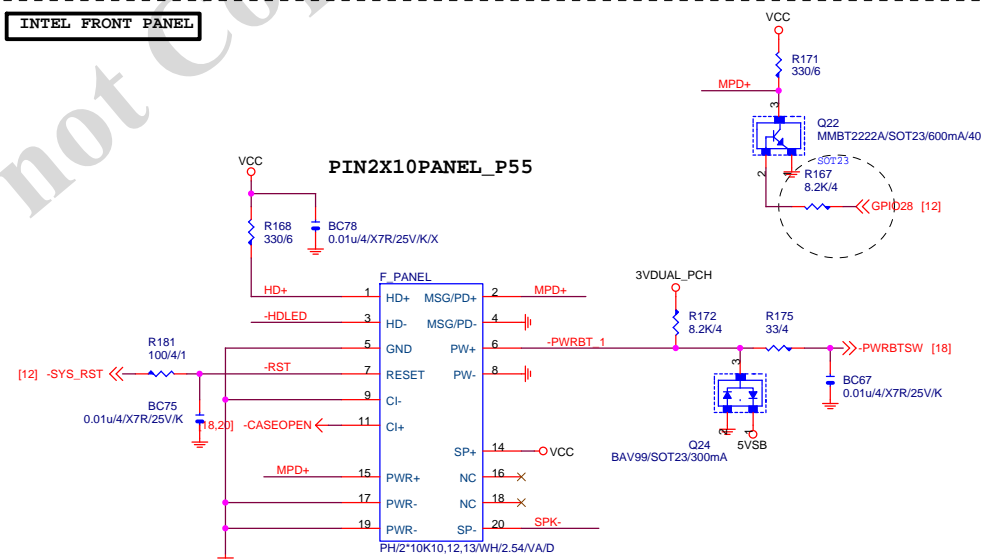
## SATA LED



## SPKR



## INTEL FRONT PANEL



Gigabyte Technology

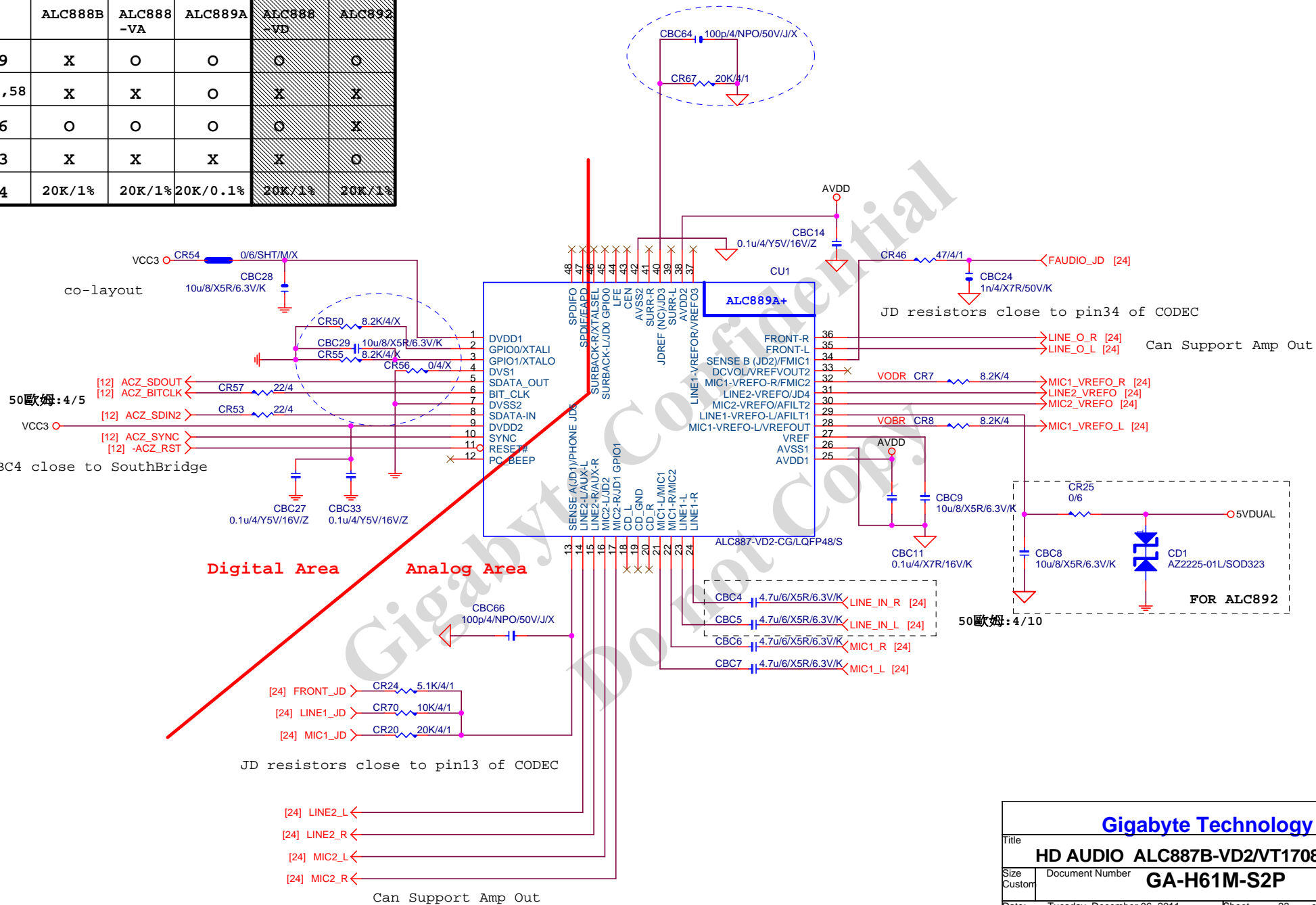
Title			FP,F_USB,USB PWR,SPKR,SATA LED	
Size			Document Number	
Custom			GA-H61M-S2P	
Date:			Tuesday, December 06, 2011	Sheet 22 of 32
				Rev 2.01



## AZALIA CODEC

ALC892/ALC889A/ALC889/ALC888B Colay

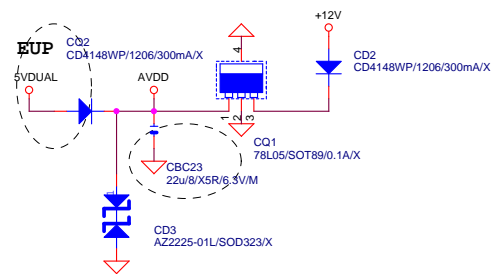
	ALC888B	ALC888 -VA	ALC889A	ALC888 -VD	ALC892
CR59	X	O	O	O	O
CR53,58	X	X	O	X	X
CR56	O	O	O	O	X
CR63	X	X	X	X	O
CR34	20K/1%	20K/1%	20K/0.1%	20K/1%	20K/1%



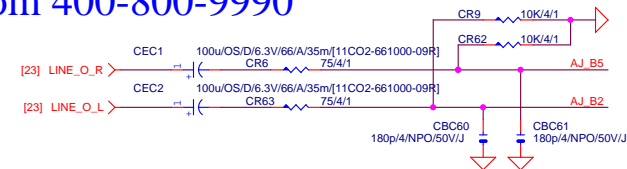
Gigabyte Technology

Title	HD AUDIO ALC887B-VD2/VT1708S/VT2021		
Size	Document Number	GA-H61M-S2P	
Custom		Rev	2.01
Date:	Tuesday, December 06, 2011	Sheet	23 of 32

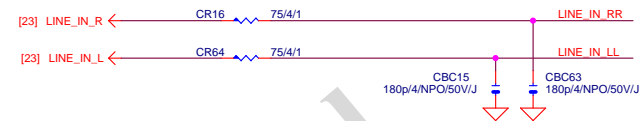
## CODEC POWER/EMI PAD



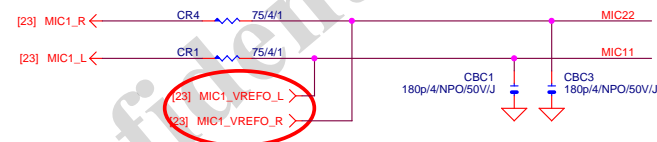
## LINE-OUT



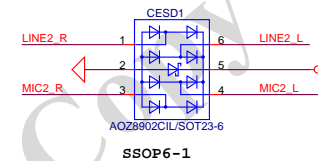
## LINE-IN



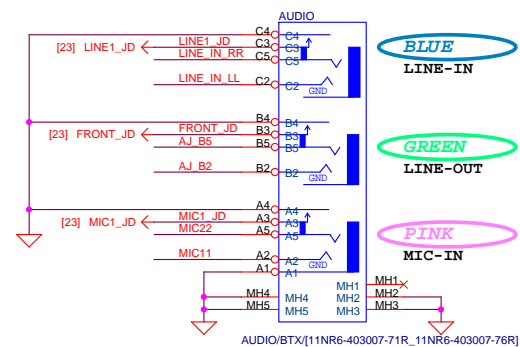
## MIC-IN



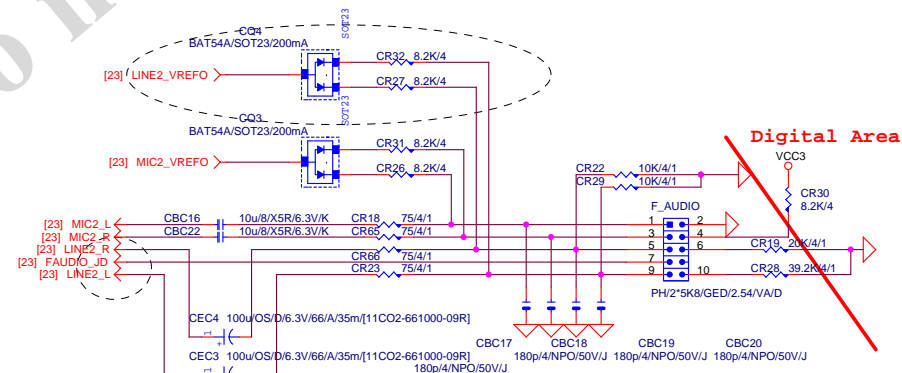
## F\_AUDIO ESI



## AZALIA JACK

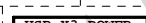
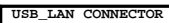
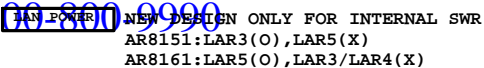
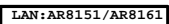


## AZALIA FRONT PANEL



Gigabyte Technology

Title			
AUDIO JACK			
Size	Document Number	GA-H61M-S2P	
Custom		Rev 2.01	
Date:	Tuesday, December 06, 2011	Sheet	24 of 32

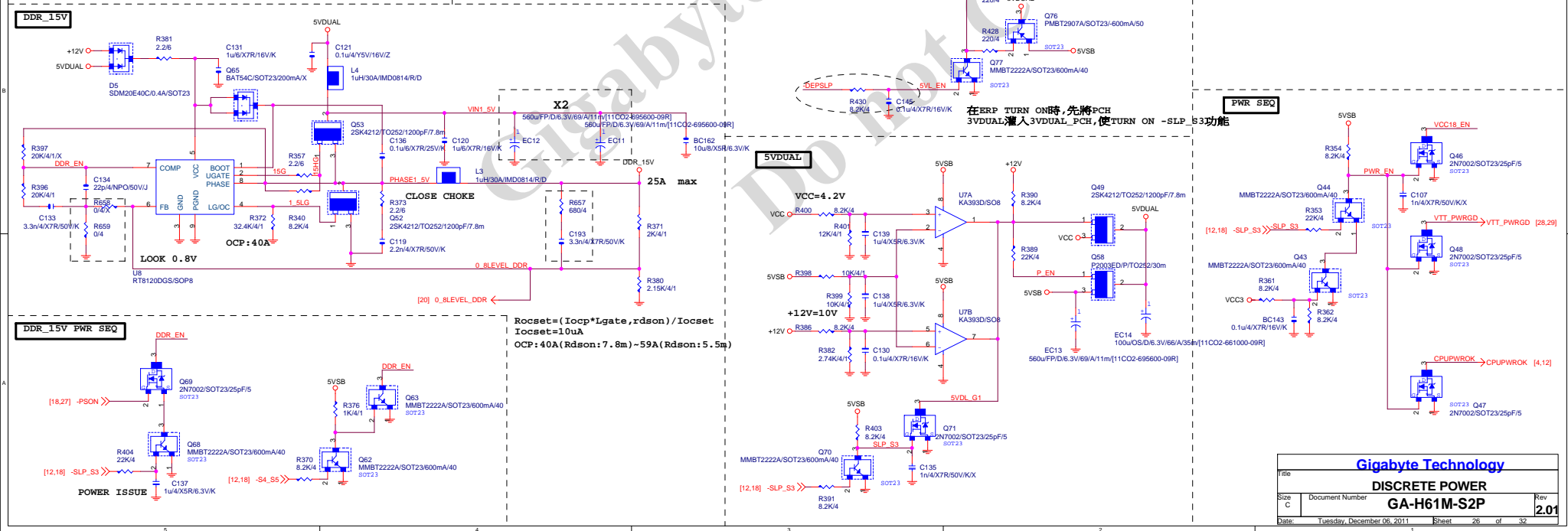
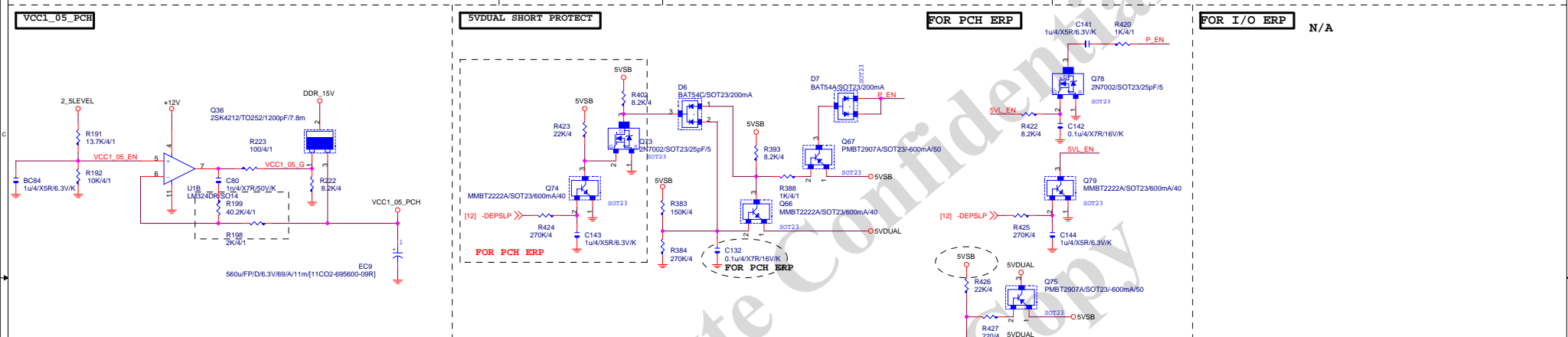
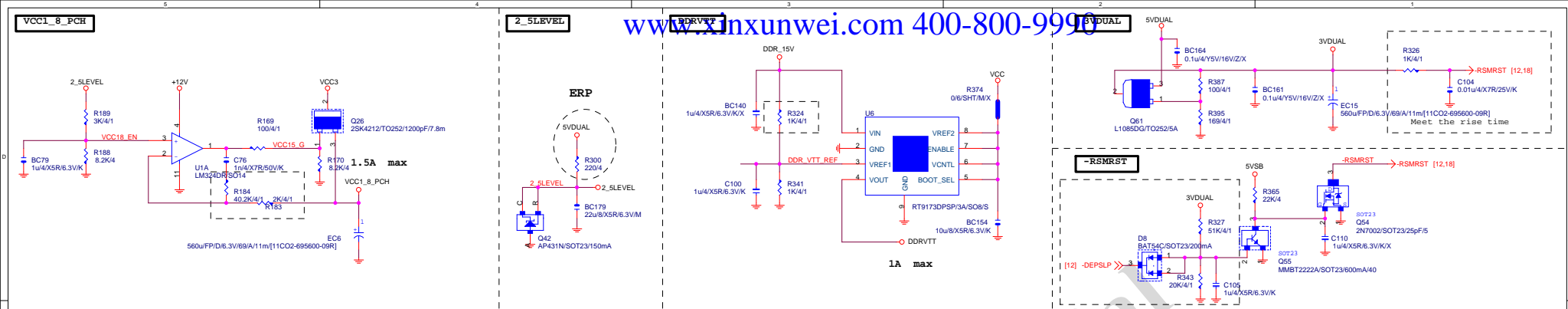


### Power domain chart

AVDD33	N/A	3.3V
VDD33	3.3V	3.3V
AVDDH	2.7V	2.7V
AVDDL/DVDDL	1.1V	1.1V
VDDCT	1.7V	

USB\_LAN BOM區分：

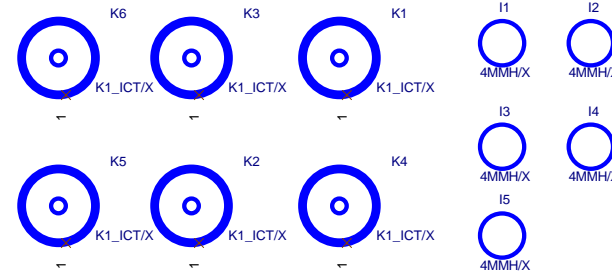
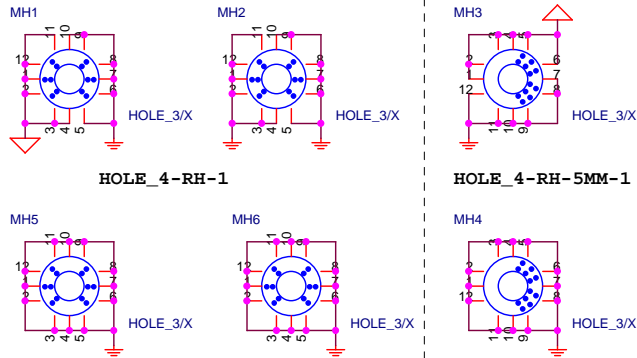
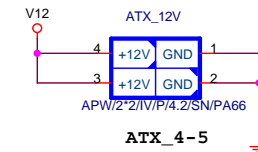
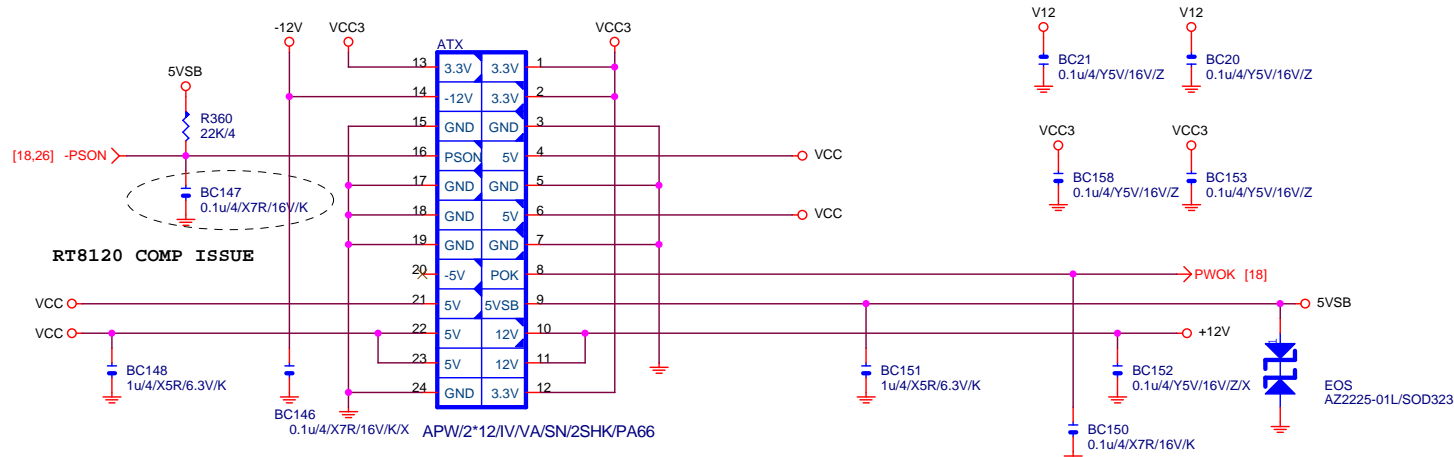
1. (紅色/12CORE/三倍):USB+LAN/1G/GO,Y/OS/RA/D/1/RE
2. (黑色/12CORE):USB+LAN/1G/GO,X/OS/RA/D/1
3. (黑色/8CORE):USB+LAN/1G/GO,Y/OS/RA/D/8C



# ATXX24 POWER CONNECTOR

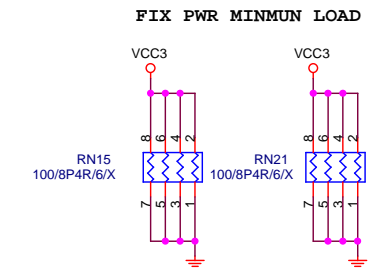
www.xinxunwei.com 400-800-9990

# ATXX4 POWER CONNECTOR



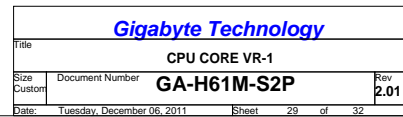
To prevent the 5VSB under loading when boot

## MIN. LOAD

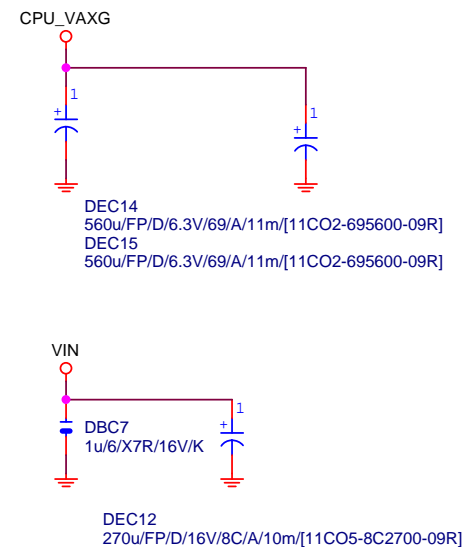


Gigabyte Technology			
ATX CONNECTOR			
Size Custom	Document Number	GA-H61M-S2P	
Date: Tuesday, December 06, 2011	Sheet	27	of 32



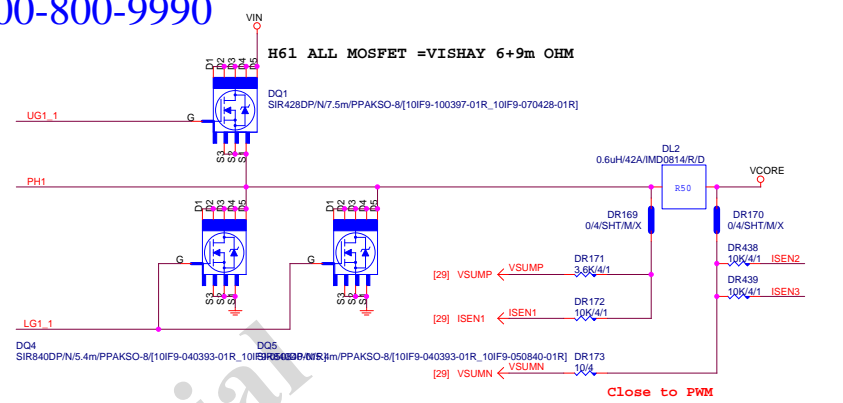
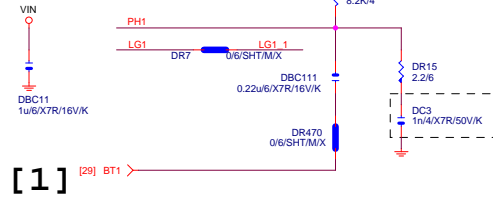






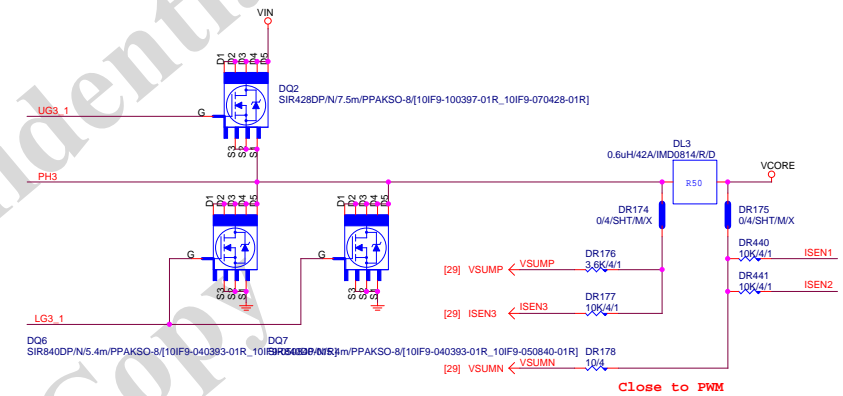
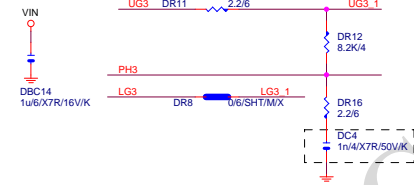
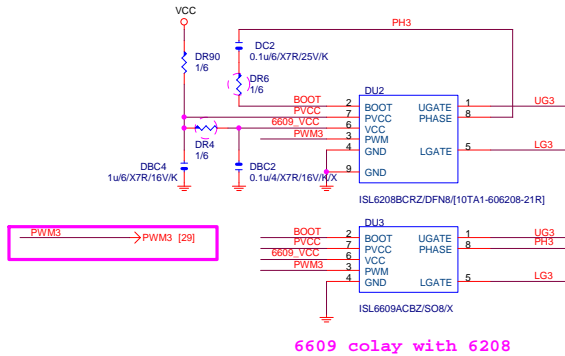
## MOS HEATSINK

## PHASE 1

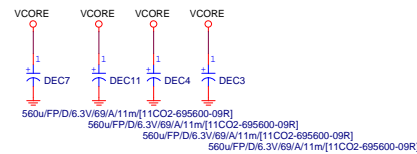
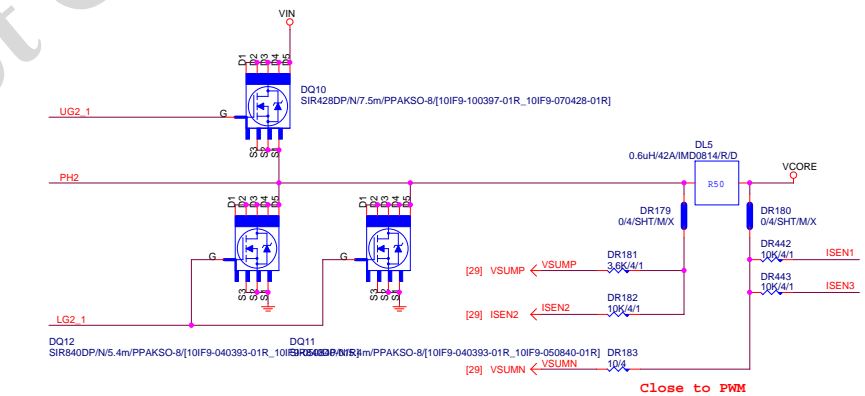
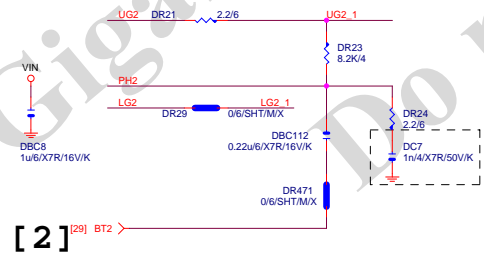
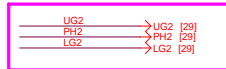


## PHASE 3

Pop ISL6625CB for PS1  
[ISL6625CBZ/S08]



## PHASE 2



Gigabyte Technology

CPU CORE VR-3

GA-H61M-S2P

Rev 2.01

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COMB

N/A

